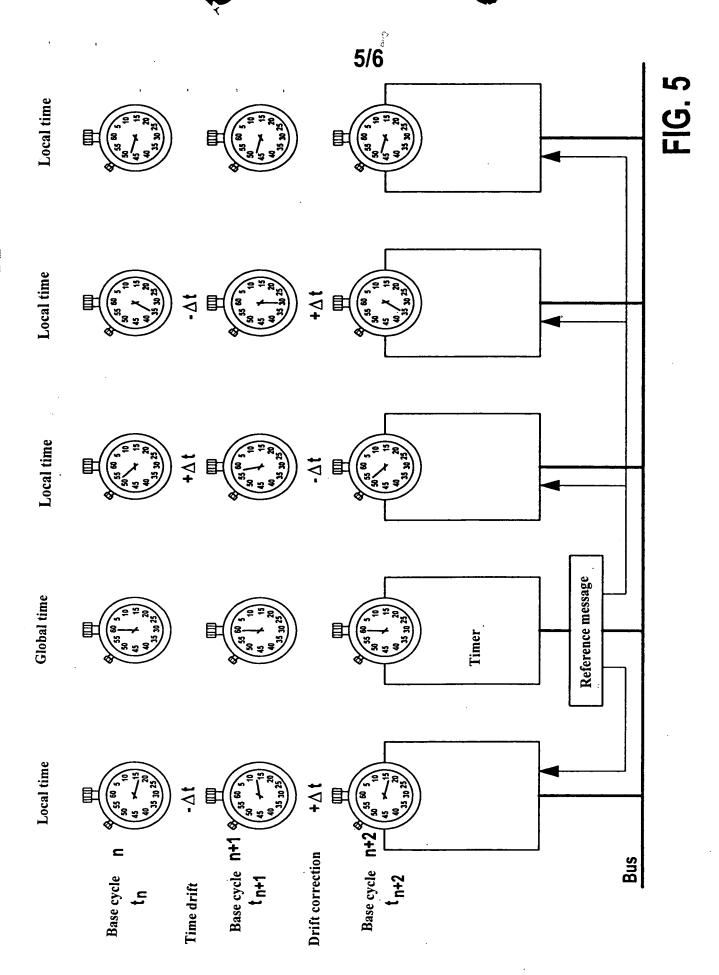


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	indow	-,									
Base cycle BZ HG, 4	ning w	ZF5a	-83-	-0-		- · · -	-80-	-ပ-		-ပ-	
	 Timing window Tii	ZF4a	405	411	417	423	429	435	441	447	·
			-4-	-4-	-4-					- V -	
			404	410	416	422	428	434	440	446	
	Timing window	ZF3a	ing				ting				
			Arbitrat		۵		Arbitra	L.	۵		
			403	409	415	421	427	433	439	445	_ _ _
(Line	Timing window	ZF2a	402 E	408 E	414 E	420 E	426 E	432 E	438 E	444 E	
	 Timing window 	ZF1a	- V -	- A -	- A -	- A -	- A -	- A -	- A -	- A -	
			401	407	413	419	425	431	437	443	
	Timing window	ZFRN (ZF0)	ime-reference message	ime-reference message	ime-reference message	ime-reference message	ime-reference message	ime-reference message	ime-reference message	ime-reference message	L «
Start cycle			BZ0a T	7	BZ2a	7	7	BZ5a	7	7	
ll cycle GZ2 Astrix)											
	Line in the matrix)	Timing window Timing window	Timing window ZFRN ZFA ZF2a ZF3a ZF4a	Timing window Timing window rindow window (ZFO) Time-reference message 401 A 402 E 403 Arbitrating 404 A 602 E 403 Arbitrating 404 A 604 C 605	Timing window Timing window Vindow Column in the matrix) ZFRN ZFRN ZF1a ZF2a ZF3a ZF4a Time-reference message 401 A 402 403 Arbitrating 404 A 401 A 406 406 409 A 401 A 408 A 401 A 4	Column in the matrix Timing window Time-reference message 401 A 402 403 Arbitrating 404 A A A A A A A A A	Column in the matrix Timing window Timin	Column in the matrix Timing window Timin	Column in the matrix Timing window Time-reference message 401	Column in the matrix Timing window Time-reference message 407 A 402 403 Arbitrating 404 A 405 B	SZ0a Time-reference message 47 A 420 440

A..F

⊈ Transmission groups
Arbitrating

⊈ reserved timing windows for arbitrating messages



Clock

Drift correction FIG. 6 (Block diagram) Start of Frame **End of Frame** Time-reference message from global timer Receive time-reference message Time-reference marker Local Buffer register time_ base Take over Local offset new Reference marker new FIFO **FIFO** Reference marker old Local offset old With respect to global **△ Offset** new **FIFO** time Reference time-reference △ Offset old message def. 0 Take over △ Offset Δ (Δ Offset) 1.B.**→** Counter **Basic correction** Drift-correction-value register 2. Message 0..3 Take over Fine correction -3...n. Message def. 0 clock (load) Result Hardware Reset Local time interval Instantaneous drift-Time-reference message Preceding sign correction value = Correction period **Enable** Reload + Addition to the residual 1 pulse 1 pulse Clock Comparator additional suppress Output generator **Z < K Enable**